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VERIFICATION OF TRANSLATION

Sir:

I, Kazunao Kubotera, residing at 2616 Ft. Fransworth Road #1C, Alexandria, Virginia 22303, declare that I am fluent in Japanese and English, and that herewith submitted English translation of U.S. Patent Application Serial No. 09/986,465 in Japanese is a true and accurate literal translation.


Kazunao Kubotera

03/14/2002.
Date

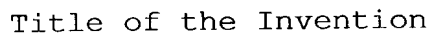


IMAGE PROCESSING, IMAGE READING APPARATUS, AND IMAGE PROCESSING
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1. Field of the Invention

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source 104 irradiates a document 100, and the reflected light is separated into the three primary colors (constituents) of R, G, and B by passing through respective filters of R (red), G (green), and B (blue). They are incident to a photoelectric conversion element
 5 CCD 101, for example such as a CCD. The light (analog data) of each color constituent incident to the CCD 101 is input respectively into an A/D converter (analog to digital converter) 102 and digitized.

[0005] Data of digitized R, G, and B is corrected respectively in a shading correction component 302, a gamma correction component
 10 303, and a line space correction component 304, and is temporarily stored in a register 305, then it is input into a matrix operation circuit of a color correction component 306. In the shading correction component 302, shading correction processing, which corrects non-uniformity of the light intensity of the light source and
 15 irregularity of the sensitivity of the CCD image elements, is applied.

In the gamma correction component 303, gamma correction processing, which corrects resolution of the respective R, G, and B data such that the output data resolution matches the resolution of the input data, is applied. In the line space correction component 305, line
 20 space correction processing, which adjusts shifting of the CCD read positions for R, G, and B, is applied.

[0006] Also, in the matrix operation circuit 306 of the color correction component, color correction processing such as a matrix operation shown below is performed using correction values for each
 25 color.

Color Correction Matrix A

$$\begin{pmatrix} R' \\ G' \\ B' \end{pmatrix} = \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{bmatrix} \begin{bmatrix} R \\ G \\ B \end{bmatrix}$$

[0007] An original image and an output image have different characteristics in color, density, and the like, because of errors
 30 in filters that separate the colors into the three primary colors, spectral sensitivity characteristics of the CCD, and image generation

characteristics of each output device (for example, printer, CRT, and the like). Thus, the image data from the CCD must be corrected by correction values. Generally, correction values R' , G' , and B' are obtained by a matrix operation, which operates on input image data using a color correction matrix A . A method of this matrix operation is disclosed in Japanese Patent Publication (KOKAI) No. H5-122455.

[0008] Specifically, as shown in FIG. 11, the color correction matrix A specified according to the output device from color correction matrices stored in lookup tables 403 is temporarily stored in register 502. This color correction matrix A and the image data of R , G , and B are input into the matrix operation circuit 306, and color correction processing is performed electronically by matrix operations. Also, the image data corrected in this manner is transferred to a computer (PC 8) via an image data bus 9 and an I/F IC, and is output to an output device via an image handling application software, or is directly output to an output component like a facsimile or a digital copy machine, or the like.

[0009] At this time, the order of processing of each color constituent in the output device depends on specifications of an image processing software in the PC, or a printer engine of a facsimile or a digital copy machine. Thus, an order of each color constituent read by CCD needs to match to an order for image processing.

[0010] Conventionally, final data after color correction is converted to an order of color constituents according to an output device by a switching circuit (an output order changing component 600) provided in an output interface component. The output order changing component 600 comprises an output order changing circuit that changes (switches) an output order based on each output device, and this output order changing component 600 requires a switching controller device.

[0011] Accordingly, the apparatus and methods of the prior art require special electrical devices for changing the output order such

corrects the input image data from the input image data storing means for each color constituent based on the correction values from the second correction value storing means; and output image data storing means that stores and then outputs the corrected image data from the correction operation means.

[0016] In the present invention, the output order of image data can be changed arbitrarily by setting the order of correction value data for color correction based on an external command. As a result, a switching circuit, a controller and memory are not required.

[0017] The data order setting means includes operation means that resorts the data order of the correction value data from the first correction value storing means; address assigning means provided between the first correction value storing means and the second correction value storing means; and memory means that stores correction values corresponding to a command that specifies the output order of the color constituents. The data order setting means is correction value specifying means that selects a single correction value from the above memory means.

[0018] Also, the present invention provides an image processing method, comprising, an image data generation step in which image data read for each of two or more color constituents is stored in input image data storing means; a correction value storing step in which correction values data for correcting the input image data for each color constituent is stored in advance in correction value storing means; a correction operation step in which the input image data read from the input image data storing means is operated on based on the correction value data read from the correction value storing means for each color constituent; and a data output step in which the image data after correction which was obtained from this correction operation step is output. The above correction value storing step includes data order setting means that changes the order of the correction value data according to a command that specifies the output order of the above color constituents.

[0019] Furthermore, the present invention provides an apparatus that can perform both color correction and monotone image creation in an image processing apparatus with a color mode and a monotone mode. The processing circuits can be simplified without requiring a special circuit for monotone image creation by executing color correction in the color mode by each of the means, and performing the operations for monotone image creation using the operation circuit for color correction in the monotone mode in which monotone image data is generated by mixing color constituents data of a color image.

[0020] Specifically, the color correction circuit has a matrix operation circuit for the color mode, and stores correction values for the monotone mode in a matrix operation format in advance.

Brief Explanation of the Drawings

[0021] FIG. 1 is a block diagram showing a structure of an image reading apparatus of a first embodiment;

FIG. 2 is a drawing showing a lookup table in which color correction matrices are stored;

FIG. 3 is a drawing showing a register 502 of the first embodiment in which the color correction matrices are stored;

FIG. 4 is a circuit drawing showing a structure of a color correction component (matrix operation circuit) of the first embodiment;

FIG. 5 is a flow chart showing an operation of the first embodiment;

FIG. 6 is a block diagram showing a structure of an image reading apparatus of a second embodiment;

FIG. 7 is a drawing showing respective lookup tables of the second embodiment in which respective correction matrices are stored;

FIGS. 8A to 8F are equations of output order change matrices V1-V6 for changing an output order of image data;

FIG. 9 is a block diagram showing a structure of the image reading apparatus of the third embodiment;

matrix Y with specific mixture ratios of each color constituent as coefficients is performed.

Y

$$\begin{pmatrix} R' \\ G' \\ B' \end{pmatrix} = \begin{bmatrix} N_{11} & N_{12} & N_{13} \\ N_{21} & N_{22} & N_{23} \\ N_{31} & N_{32} & N_{33} \end{bmatrix} \begin{bmatrix} R \\ G \\ B \end{bmatrix}$$

- 5 **[0027]** The monotone image matrix Y calculates R', G', and B' as a mixture of each color constituent, and one of R', G' and B' is selected and used as monotone image data. Accordingly, the monotone matrix Y is constituted by coefficients that determine the mixture ratio of each color constituent.
- 10 **[0028]** The CPU 5 has a register 502 (second correction value storing means) that temporarily stores a matrix output from the lookup tables 403 and an address assigning component (address assigning means) 503. The address assigning means 503 assigns addresses when performing input and output of color correction matrices between the lookup tables
- 15 403, the register 502, and the color correction circuit 306. The register 502, as shown in FIG. 3, temporarily stores nine data in the respective addresses from 0H to 8H, and each matrix output from the lookup tables 403 in the addresses 0H-9H. The register 502 then outputs to the matrix operation circuit of the color correction
- 20 component 306.
- [0029]** As shown in FIG. 1, the gate array 3 comprises a shading correction component 302, a gamma correction component 303, a line space correction component 304, a register 305 (input image data storing means), a matrix operation circuit 306 (correction operation
- 25 means), a register 309 (output image data storing means) and a controller 307. The shading correction component 302 applies shading correction to image data that was digitally converted via the CCD 101 and the A/D converter 102, to rectify the image data by eliminating non-uniformity of light intensity of the light source and irregularity
- 30 of sensitivity of each image element of the CCD, and the like. The gamma correction component 303 applies gamma correction to correct

each level of R, G, and B image data such that the output levels are directly proportional to the levels of the input image data. The line space correction component 304 corrects shifting of the read positions of R, G, and B among the various CCD line image sensor sensors
 5 CCD 101. The register 305 (input image data storing means) includes a circuit for temporally storing the image data after the line space correction. The matrix operation circuit 306 (correction operation means) applies color correction processing to the image data from the register 305. The register 309 (output image data storing means)
 10 includes a circuit for temporally storing the image data after the color correction. The controller 307 controls timing of each color processing.

[0030] The matrix operation circuit of the color correction component 306, as shown in FIG. 4, is constituted by nine
 15 multiplication circuits 51-59 and three addition circuits 60-62, and they perform matrix operations using the matrix output from the register 502 and the image data output from the register 305, and compute the image data R', G', and B'.

[0031] Specifically, when using the color correction matrix A1
 20 (shown below) stored in the register 502 to perform the matrix operation (shown below), as shown in FIG. 3 and FIG. 4, the coefficient K_{11} stored in the address 0H in the register 502 is input into the multiplication circuit 51. Likewise, the coefficient K_{12} stored in the address 3H in the register 502 is input into the multiplication
 25 circuit 52, and the coefficient K_{13} stored in the address 6H in the register 502 is input into the multiplication circuit 53. After multiplications $K_{11} * R$, $K_{12} * G$, and $K_{13} * B$ are performed respectively, the result is added by the addition circuit 60, and R' is output. G' and B' are computed in the same manner. All computed data are
 30 temporarily stored in the register 309, and then output in the order of R', G', and B' to the output device 2 via the PC 8.

$$A1 = \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{bmatrix}$$

$$\begin{pmatrix} R' \\ G' \\ B' \end{pmatrix} = \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{bmatrix} \begin{bmatrix} R \\ G \\ B \end{bmatrix}$$

[0033] First, as shown in FIG. 1, the color mode is selected using the operation component 801 of the PC 8 (Step S1).

[0035] The CPU 5 receives matrix specification data, which specifies the color correction matrices corresponding to the output device 2, and output order data, which specifies the output order of R, G, and B, from the PC 8 (Step S3). Following these specification data, the address assigning component 503 selects from the lookup table 403(a) the color correction matrix A which corresponds to the output device 2, for example the color correction matrix A1, and outputs to an assigned address in the register 502 (Step S4).

in the addresses 1H, 4H, and 7H in the lookup tables 403 is output to the addresses 2H, 5H, 8H in the register 502, and each of the coefficients K_{31} , K_{32} , and K_{33} in the addresses 2H, 5H, and 7H is output to the addresses 1H, 4H, and 7H in the register 502. Through this process, as shown below, the second row and the third row in the color correction matrix A_1 are exchanged, and the color correction matrix A' with the exchanged rows is stored in the register 502.

$$A_1 = \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{bmatrix} \xrightarrow[\substack{\text{row 3} \rightarrow \text{row 2} \\ \text{row 2} \rightarrow \text{row 3}}]{\text{Row Exchange}} A' = \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{31} & K_{32} & K_{33} \\ K_{21} & K_{22} & K_{23} \end{bmatrix}$$

10 **[0037]** The color correction matrix A' obtained in this manner can change the mixture ratio of each image data in the color correction processing as well as the output order.

[0038] Next, the color correction processing is performed in the color correction component 306 using the color correction matrix A' in the register 502.

[0039] The image data R , G , and B stored in the register 305 after color corrections and the color correction matrix A' stored in the register 308 are input into the matrix operation circuit of the color correction component 306 (Step S5), and the matrix operation of the color correction processing is performed (Step S6).

A_1'

$$\begin{pmatrix} R' \\ G' \\ B' \end{pmatrix} = \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{31} & K_{32} & K_{33} \\ K_{21} & K_{22} & K_{23} \end{bmatrix} \begin{bmatrix} R \\ G \\ B \end{bmatrix}$$

[0040] Specifically, as shown in FIG. 3 and FIG. 4, the coefficient (K_{11}) stored in the address 0H in the register 502 is input into the multiplication circuit 51. Likewise, the coefficient (K_{12}) stored in the address 3H in the register 502 is input into the multiplication circuit 52, and the coefficient (K_{13}) stored in the address 6H in the register 502 is input into the multiplication circuit 53. Then, the multiplications (K_{11}) * R , (K_{12}) * G , and (K_{13}) * B are performed

respectively by multiplication circuits 51, 52 and 53, and (R') is obtained by adding the results in the addition circuit 60. (B') and (G') are obtained in the same manner. The results are temporarily stored in the register 309, and then they are output in the order
 5 of (R'), (B'), and (G') to the output device 2 via the PC 8 (Step S7).

[0041] Next, the case of the monotone mode is explained.

[0042] First, as shown in FIG. 1, the monotone mode is selected using the operation component 801 of the PC 8 (Step S1).

10 **[0043]** Next, similar to the color mode, the reflected light from the document 100 irradiated by the light source 104 is separated into each of the color constituents R, G, and B by color filters, and is incident on the CCD 101. The light (analog data) incident on the CCD 101 is converted into digital data by the A/D converter 102.
 15 After processed by the shading correction component 302, the gamma correction component 303, and the line space correction component 304, the digital data is stored in the register 305 (Step S10).

[0044] Next, the CPU 5 receives matrix specification data, which specifies a monotone matrix corresponding to the output device 2,
 20 from the PC 8 (Step S11). Also, following this matrix specification data, the address assigning component 503 selects one monotone matrix Y (for example, Y1) from the monotone lookup table 403(b), and outputs to the register 502 (Step S12).

[0045] This output is not illustrated, but respective constituents
 25 N_{11} - N_{13} stored in the addresses 0H to 8H in the lookup table 403(b) are output to the addresses 0H to 8H in the register 502 in the same order that they are stored in the lookup table 403(b).

[0046] Next, the monotone matrix Y1 in the register 502 is output to the color correction component 306, and monotone image creation
 30 is performed by the color correction component 306.

[0047] The image data R, G, and B stored in the register 305 after color corrections and the monotone matrix Y stored in the register 308 are output to the matrix operation circuit of the color correction

matrices V (V1, V2...V6), and the monotone matrices Y (Y1, Y2...) stored in the respective lookup tables 403. FIGS. 8A to 8F show the changing matrices V1-V6 that change the output order of each color constituent according to the output order of the image data R, G, and B changed by matrix operations.

[0056] The image reading apparatus 1 has a color mode to output color image data in color and a monotone mode to output in monotone, and selection of these modes is performed by an operation component 801 (image processing mode selection means) in the PC 8. The mode signal selected by the operation component 801 is transmitted and received via the I/F IC 10 that controls the transmission and receipt timing of data with the PC 8.

[0057] Also, just as in the first embodiment, the color image reading apparatus 1 has an image reading component that is constituted of a light source 103 that irradiates a document 100, a line image sensor CCD 101 as a photoelectric conversion element, and an A/D converter 102 that converts the analog data from the CCD 101 into digital data. The CCD 101 photo-electrically converts the reflected light from the document 100 through an optical system to electrical signal according to each of the color constituents R, G, and B. Furthermore, a gate array (G/A) 3 comprises processing circuits, and the like, which apply various processing to the image data from the A/D converter 102, a ROM 4 that stores color correction matrices, and a CPU 5.

[0058] As shown in FIGS. 6 and 7, lookup tables 403 (first correction value storing means) are stored in the ROM 4, and lookup tables 403 are constituted by the color lookup table 403(a), an output order changing lookup table 403(b), and a monotone lookup table 403(c).

[0059] As shown in FIG. 7, the color lookup table 403(a) stores color correction matrices A (A1, A2...), which are color correction values correspond to image characteristics of various output devices in each specified address. A color correction matrix A is a 3x3 matrix, and constituted by coefficients (correction value data) that determine

specifying the color correction matrix corresponding to the image characteristics of the output device 2, and the output order data indicating the output order of R, G, and B, from the PC 8. One color correction matrix A (for example, A1) corresponding to the output device 2 is selected from the color correction lookup table 403(a) according to these data and the address assigning component 503. One output order changing matrix V (for example, V2) is selected according to the output order data from the output order lookup table 403(b), and they are output to the operation component 501.

[0069] In the operation component 501, the matrix operation shown below is performed. Through this operation, the color correction matrix A1 is subjected to row exchange (resorting of the data order).

Also, a combined matrix C for determining the mixture ratio of each color constituent in color correction processing and for changing the output order is obtained. The results are output to the register 502.

$$\begin{array}{ccc} \text{Combined Matrix C} & \text{V2} & \text{A1} \\ \left(\begin{array}{ccc} K_{11} & K_{12} & K_{13} \\ K_{31} & K_{32} & K_{33} \\ K_{21} & K_{22} & K_{23} \end{array} \right) & = \left[\begin{array}{ccc} 1 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 1 & 0 \end{array} \right] \left[\begin{array}{ccc} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{array} \right] \end{array}$$

[0070] Next, in the color correction component 306, using the combined matrix C output from the register 502 and the image data R, G, and B output from the register 305, the matrix operation shown below is performed. The color corrected image data R', B' and G' is temporarily stored in the register 309 (output image data storing means), and then it is output in this order to the output device 2 via the PC 8.

$$\begin{array}{ccc} \text{Combined Matrix C} \\ \left(\begin{array}{c} R' \\ G' \\ B' \end{array} \right) & = \left(\begin{array}{ccc} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{array} \right) \left(\begin{array}{c} R \\ G \\ B \end{array} \right) \end{array}$$

[0071] Next, the monotone mode is explained.

[0072] In the case of the monotone mode, the CPU 5 receives the

data specifying the color correction matrix corresponding to the output device 2 and the data specifying the monotone matrix from the PC 8. One color correction matrix A (for example, A1) corresponding to the output device 2 is selected from the color correction lookup table 403(a) according to these data and the address assigning component 503. One monotone matrix Y (for example, Y1) is selected according to the output device 2 from the output order lookup table 403(c), and they are output to the operation component 501.

[0073] In the operation component 501, the operation shown below is performed. A combined matrix D with a function of determining the mixture ratio of each color in color correction processing and determining the mixture ratio of each color constituent in monotone processing is obtained, and it is output to the register 502.

$$\begin{array}{ccc} \text{Combined Matrix D} & Y1 & A1 \\ \begin{pmatrix} M_{11} & M_{12} & M_{13} \\ M_{21} & M_{22} & M_{23} \\ M_{31} & M_{32} & M_{33} \end{pmatrix} & = & \begin{bmatrix} N_{11} & N_{12} & N_{13} \\ N_{21} & N_{22} & N_{23} \\ N_{31} & N_{32} & N_{33} \end{bmatrix} \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{bmatrix} \end{array}$$

[0074] Next, in the color correction component 306, using the combined matrix D output from the register 502 and the image data R, G, and B output from the register 305, the matrix operation shown below is performed. The color corrected image data R', B' and G' is temporarily stored in the register 309, and then any one of R', B' and G' is output to the output device 2 via the PC 8 as monotone image data.

$$\begin{array}{ccc} \text{Combined Matrix D} & & \\ \begin{pmatrix} R' \\ G' \\ B' \end{pmatrix} & = & \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{bmatrix} \begin{bmatrix} R \\ G \\ B \end{bmatrix} \end{array}$$

[0075] As explained above, according to the present invention, matrix operations are performed with a color correction matrix and an output order matrix corresponding to an output device in the color mode, or with a color correction matrix and a monotone matrix corresponding to an output device in the monotone mode. A combined

in color and a monotone mode which outputs in monotone, and selection of these modes is performed by an operation component 801 (image processing mode selection means) of the connected PC 8. The mode signal of the mode selected by the operation component 801 is transmitted and received via the I/F IC 10 that controls the timing of transmission and receipt of data with the PC 8.

[0079] Also, the image reading apparatus provides with ROM 4. As shown in FIG. 10, the ROM 4 has lookup tables 404 (first correction value storing means), and lookup tables 403 are constituted by a combined C matrix lookup table 404(a) and a combined D matrix lookup table 404(b).

[0080] As shown in FIG. 10, in the combined matrix C lookup table 404(a), combined matrices C (C1, C2...) are respectively stored in the specified addresses. The combined matrices C are the products of the six output order changing matrices V (V1, V2...V6) shown in FIG. 8 that change the output order of the image data, and the color correction matrices A (A1, A2...) that correspond to output devices.

For example, as shown below, the combined matrix C1 can be obtained by multiplying the color correction matrix A1 by the output order matrix V2.

$$\begin{array}{ccc} \text{Combined Matrix C1} & \text{V2} & \text{A1} \\ \left(\begin{array}{ccc} K_{11} & K_{12} & K_{13} \\ K_{31} & K_{32} & K_{33} \\ K_{21} & K_{22} & K_{23} \end{array} \right) & = & \left[\begin{array}{ccc} 1 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 1 & 0 \end{array} \right] \left[\begin{array}{ccc} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{array} \right] \end{array}$$

[0081] Each combined matrix C has the functions of changing the mixture ratio of each color constituent in color correction corresponding to the output device as well as determining the output order. In other words, they are the products of row exchange corresponding to the output orders. A combined matrix C is a 3x3 matrix, and constituted by coefficients (correction value data) that determine a mixture ratio of each color constituent. For example, in the case of the combined matrix C1, it is constituted by nine coefficients from K₁₁ to K₃₃, where K₁₁, K₂₁, and K₃₁ correspond to R,

K₁₂, K₂₂, and K₃₂ correspond to G, and K₁₃, K₂₃, and K₃₃ correspond to B, respectively.

[0082] In the combined matrix D lookup table 404(b), combined matrices D (D₁, D₂...), which are products of multiplied the monotone image matrices Y (Y₁, Y₂...) and each of color correction matrices A which correspond to various output devices, are stored in the specified addresses. For example, as shown below, the combined matrix D₁ is a product of the color correction matrix A₁ and the monotone image matrix Y₁.

$$\begin{matrix} 10 & \text{Combined Matrix D1} & Y1 & A1 \end{matrix}$$

$$\begin{pmatrix} M_{11} & M_{12} & M_{13} \\ M_{21} & M_{22} & M_{23} \\ M_{31} & M_{32} & M_{33} \end{pmatrix} = \begin{bmatrix} N_{11} & N_{12} & N_{13} \\ N_{21} & N_{22} & N_{23} \\ N_{31} & N_{32} & N_{33} \end{bmatrix} \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{bmatrix}$$

[0083] Each combined matrix D has the functions of changing the mixture ratio of each color constituent in color correction corresponding to the output device and determining the mixture ratio of the monotone image color constituents.

[0084] The CPU 5 has a register 502 (second correction value storing means) that stores matrices output from the combined matrix C lookup table 404(a) or the combined matrix D lookup table 404(b), and an address assigning component 503 (correction value specifying means) that selects matrices from the lookup table 403(a) or the lookup table 403(b) by specifying the addresses and outputs to the register 502.

The register 502, as shown in FIG. 3 explained in the first embodiment, temporarily stores nine data in the respective addresses from 0H to 8H, and each matrix output from the lookup tables 403 in the addresses 0H-09. The register 502 then outputs to the matrix operation circuit of the color correction component 306.

[0085] Next, an operation of the image reading apparatus 1 of the above constitution is explained.

[0086] As shown in FIG. 8, first, either one of the color mode or the monotone mode is selected using the operation component 801 of the PC 8.

[0087] Same as in the first embodiment, the light (analog data) reflected from the document 100 and separated to each of the color constituents R, G, and B passes through the CCD 101, the A/D converter 102, the shading correction component 302, the gamma correction component 303, and the line space correction component 304. The image data then is temporarily stored in the register 305 (input image data storing means).

[0088] In the case of the color mode, the CPU 5 receives the data specifying the color correction matrix corresponding to the image characteristics of the output device 2, and the output order data indicating the output order of R, G, and B, from the PC 8. One combined matrix C (for example C1) is selected from the color correction lookup table 404(a) according to these data by the address assigning component 503. The combined matrix C is then output to the operation component 501 without row exchange as seen in the first embodiment.

[0089] Next, in the color correction component 306, using the combined matrix C1 output from the register 502 and the image data R, G, and B output from the register 305, the matrix operation shown below is performed. The color corrected image data R', B' and G' are temporarily stored in the register 309 (output image data storing means), and then they are output in this order to the output device 2 via the PC 8.

C1

$$\begin{pmatrix} R' \\ G' \\ B' \end{pmatrix} = \begin{bmatrix} K_{11} & K_{12} & K_{13} \\ K_{31} & K_{32} & K_{33} \\ K_{21} & K_{22} & K_{23} \end{bmatrix} \begin{bmatrix} R \\ G \\ B \end{bmatrix}$$

[0090] In the case of the monotone mode, the CPU 5 receives the data specifying the color correction matrix corresponding to the output device 2 and the data specifying the monotone matrix from the PC 8. Base on these data, the address assigning component 503 selects one combined matrix D (for example, D1) corresponding to the output device 2 from the combined matrix D lookup table 404(b), and outputs it to the register 502.